ELLIOTT 903 ALGOL

OBJECT CODE MANUAL (JUNE 1966)

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INTRODUCTION

The purpose of this manual is to describe the object code of the Elliott 903 Algol system. The elements of this code are called "pords" (parameter words) and are output on to paper tape by the Algol translator while it is reading the source program on an input paper tape. These pords are interpreted one at a time at run time.

The object code is based on one described in the book "Algol 60 Implementation" by Randell and Russell, but is by no means identical to it.

A run time stack is used, somewhat like that in the book, in order to evaluate expressions and to deal with the execution of blocks, procedures and for statements. Although a number of features, for example strings, are handled identically in Randell and Russell and in 903 Algol, it has been possible to introduce certain simplifications because of the restrictions imposed by the IFIP subset. Other changes have also been made and the principal differences are:-

- 1. The absence of recursion in the subset permits scalars to be given fixed addresses in the store; arrays cannot be treated in this way because their size is not known until run time.
- 2. The treatment of labels and switches is simplified by the absence of designational expressions from switch lists and by the requirement to declare labels in switch lists; this latter is <u>not</u> an IFIP restriction.
- 3. The type of an expression, i.e. whether it is real, integer or boolean can be decided at translation time because of the rules of the subset. Unfortunately the parameters to formal procedures form an exception to the rules and it was decided to check at every procedure entry that the actual/formal correspondences are legal; this check extends to include the number of parameters or dimensions of a procedure or array parameter respectively.
- 4. The restricted call-by-name in the subset removes the need for the "implicit subroutines" described in the book.
- 5. The addressing of parameters is not as elegant as the method described in the book; although the vector called "display" has been removed, a search down the stack at each parameter reference is sometimes necessary.
- 6. The object program address increases by one word at a time rather than by one syllable at a time.

- 1`-

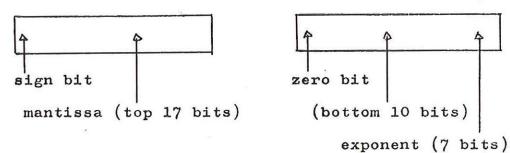
- 7. The object program does not accumulate in the store as translation proceeds but is punched out on paper tape (in relocatable binary form). There is therefore no problem in allocating space to hold the object program, but there is the new problem that one cannot alter something which has already been punched. This is overcome by making use of some facilities of the loader program which performs a second pass as it stores the pords (see 5.2 below).
- 8. Input and output operations which correspond to all the features of read and print lists have direct counterparts in the pords.
- 9. An object program block is set up for a procedure body, a for statement, or a block which contains an array or switch declaration.) The case where time is saved is where only <u>local</u> variables are declared and this, unfortunately is rare in practice.

FORMAT OF REAL, INTEGER AND BOOLEAN VARIABLES

2.1 Real

2.

In the store a real, or an element of a real array occupies two words with the left (more significant) word at an address one lower than the right hand word. There is no restriction for the left hand word to be at an odd or even address.



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With mantissa = a and exponent = b a number of value x is represented as $x = a \times 2^{b}$ with for $x \neq 0$ $-l \leq a < -\frac{1}{2}$ $\frac{1}{2} \leq a < 1$ $-64 \leq b < 63$ for x = 0 , a=0 , b=0

- 2 -

In this form it is said to be packed. It is to be noted that the most significant digit of the exponent acts as a sign bit so that if b = -63 the digits are

1000 001

The largest number which can be held is 9.223 x 10 approximately and the accuracy is to 8 significant decimal digits (27 bits plus sign bit).

When a real number is brought to the stack it is unpacked into three locations with the exponent occupying the third location and the exponent part of word two cleared to zero. Intermediate results are therefore held to an accuracy of 34 bits plus sign; round off occurs when the result is assigned to a location pair in the store.

2.2 Integer

An integer occupies a single word of 18 bits and lies between -131072 and +131071.

2.3 Boolean

3.

True is represented by the value +1 and false by the value zero; in either case a whole word is used to hold the value.

THE STACK

The stack is basic to the operation of the system and is used to hold, among other things, the intermediate results formed during the evaluation of an expression.

3.1 Assignment statement

If a, b and c are all real then the Algol text

a:=b+c;

is translated into the pords (for explanation see 6 below):-

 TRA
 "a"

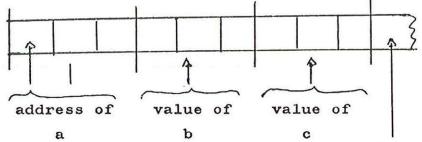
 TRR
 "b"

 TRR
 "c"

 PRIM
 R+R→R

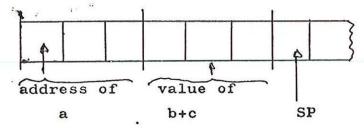
 PRIM
 ST

The interpreter scans down the pords and, in the case of data references, places the address or the value on the stack; three locations are allotted to each entity in the stack. After scanning the first three pords therefore the stack looks like this:-



Stack Pointer, SP points to next free set of locations.

A "PRIM" pord defines an operation, in this case the addition of two reals; after this pord the stack looks like this:-



Finally the "PRIM ST" pord assigns the value of b+c to a and moves the stack pointer, SP, back to where it was before the statement began. The stack therefore does not grow continuously but returns to a standard place at the end of each statement.

3.2 Procedure calls

When a procedure is entered in Algol it is necessary to store the return address from which the procedure was called. The address within the pord program is called the pord pointer, PP and this is preserved in the stack at the point of call since it indicates the next pord to be taken after execution of the procedure.

In fact it is necessary to preserve four quantities in the stack in order to enter and leave a procedure correctly; these four are:-

Entry pointer	EP	Points to the start of the four locations (called a "stack entry") which hold the status of the <u>calling</u> block.
Pord pointer	PP	Points at next pord for a return address.
EP' - 3m		Where there are m parameters,

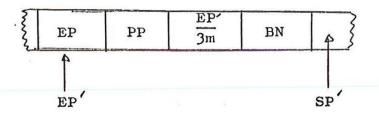
Where there are m parameters, this address is the value to which the stack pointer must return after exit from the procedure.

- 4--

Block number BN

This is a unique 9 bit integer for each run time block. Its presence in the stack enables a parameter to be referenced by searching down the stack if necessary. It also allows a "go to" to leave the procedure body.

The stack entry comprises all of these as shown below, primes denote quantities of the current block and unprimed quantities refer to the calling block.



The first two locations are filled in at the point of call and the last two at the procedure's entry point.

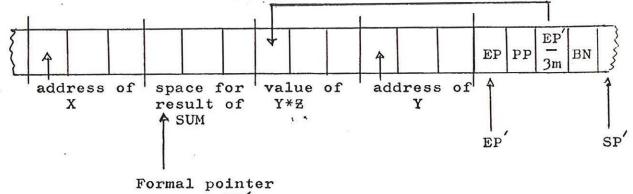
The parameters to a procedure and the space for its result, if any, are also held in the stack. Parameters called by value are developed in the stack as values at the point of call and parameters called by name have their addresses in the stack.

Example

begin integer procedure SUM (A,B); value A; integer A,B; SUM:= A+B; integer X, Y, Z;

$$X := SUM (Y \times Z, Y):$$

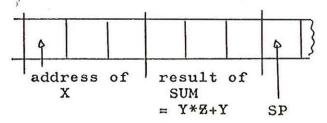
After the entry to SUM the stack contains



FP'

- 5 -

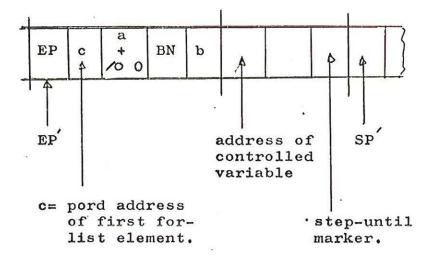
During the body of SUM reference is made to another pointer called the formal pointer FP. This points to the result space if a type procedure is involved and a parameter numbered n is always referenced by the item stored at FP + 3n. FP is set up by the pord at the entry to the procedure body. After exit from the body the stack contains



ready for the assignment to X to be made.

3.3 For-statements

A for-statement is made into a run time block with the help of a stack entry involving five quantities and this is always followed by the address of the controlled variable:-



The address 'a' is the address of the controlled statement and the address b is that of the next statement. The sign bit of the word containing 'a' is set equal to one to mark the entry as a forblock in case a go to causes an exit from the block.

A while or step-until element continually executes the statement beginning at'a which eventually returns control to c'the for-list element in question where evaluation of the condition begins again; during this evaluation the stack is used as workspace in the normal way with the help of SP. When the element is exhausted the address c gets overwritten with that of the next for-list element and so the process goes on.

- 6 -

A simple for-list element merely overwrites c before execution of the controlled statement.

The last for-list element is followed by a special pord which causes exit from the entire for-block and execution resumes at pord address b.

The location at EP+7 is the step-until marker used to prevent the incrementing of the controlled variable at the first iteration. It is important that BN is stored in the same relative position to EP for a procedure and a for-block stack entry because of parameter references and go to.

3.4 Arrays

4.

An array has space set aside for it in the stack just after a block entry in which it has been declared. For the details of this action see MAMPS in 6.4 below. An array parameter called by value causes all the elements of an array to be copied from one part of the stack to another (see 8 below).

STORAGE ALLOCATION

For a basic machine there are alternative ways of dividing up the store depending on whether the library of Algol procedures is held after the end of the interpreter or is held as a relocatable binary paper tape.

With a multi module machine it would appear reasonable to confine the interpreter, library and special machine code to module 1 and to place the object code etc in module 2 onwards. The only restriction is that the length of the object code, constants and scalars must not <u>individually</u> exceed 8191 words.

Figure 1 shows the case of a basic machine containing the whole of the library in the store. Dealing with the areas in turn:-

4.1 Interpreter

This part is a machine code program which contains the subroutines for input/output, the simpler Algol standard procedures, floating point arithmetic, interpretation of the pord code and management of the run time stack. The interpreter is aware of the starting address of the pord program even if the library changes size because the pord program is loaded by entry at a special address.

- 7

4.2 The library

This consists of the machine coded Algo1 procedures:-

arctan cos sin sqrt instring outstring lowbound range

under library name "qatrig"

under library name "qastri"

The linking of these procedures to their calls from within the object code is done by the loader program which acts in a similar manner to that of the SIR loader. It follows therefore that all the above names including qatrig and qastri are held in the loader's dictionary before the relocatable Algol object code tape is fed in.

If the library gets deliberately overwritten by input of an object program it can be re-established by a start at 12; this also resets the name into the loader's dictionary.

4.3 The pord code

This is described in detail in the rest of this manual. Each word has, like machine code, a function part of 5 bits and an address part of 13 bits and these words are examined in turn by the interpreter. All the operations required by the Algol source text are done by interpreter subroutines which are reached from the pords.

4.4 Constants

Each constant mentioned in the Algol text is stored here (once only) as a positive integer or a positive floating point number. Information relating to switches and labels is also held here and the contents of the whole area are punched out at the end of a translation.

This part of the output tape is preceded by the left hand global name "qacodl" (Algol Constants Object Data Load) and this is noted by the loader. All references to constants, labels or switches at run time require the interpreter to refer to the address of qacodl.

8

4.5 Scalars

Each integer, boolean or real variable declared in the source text has space reserved for it here. The area is labelled globally by the name "qavnda" (Algol Variables Notional Data Area) and this is also noted by the loader. All references to these variables at run time require the interpreter to refer to the address of qavnda.

The space is reserved by using a SIR skip code and hence the area is not cleared to zero during input, nor is it cleared to zero at the start.

This scheme is not so efficient as one which places all scalars on the stack, but the loss of space is never serious in practice and there is a slight gain in speed.

4.6 Machine code

A User may wish to have special procedures written in machine code and these can be input by a start at 11 after input of the Algol pord program.

4.7 Stack

As described in section 3.

4.8 Loader names

This is a list of names and addresses which is used by the loader to link procedures to their calls.

4.9 Loader

This loads relocatable binary paper tape.

4.10 Systems area

No use is made of this area at present. It could be used for transferring the Algol translator or interpreter from magnetic tape or disc into the store.

4.11 Summary

There is better use of available storage if the Algol is translated in "library mode" (start at 12). Figure 2 shows the layout. In the first place it is likely that the program does not need all the library and only the part which is needed is present; in the second place the space needed for scalars can overlap the loader but to take advantage of this the scalar space must come last. This is what happens when a translation is done in library mode. Figure 3 shows a suggested use of store in a multi module machine.

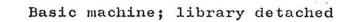
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		down	abiequ		
			Address 8	Remarks	
	INTERPRETER	2			
-					
		•			
			15		
	LIBRARY		4200		
	PORD OBJECT CODE		4900		1 6 8
			(000		
	CONSTANTS		6000	Labelled Q.	
	SCALARS		6200	Labelled Q.	
	MACHINE CODE		6400	If any is	present
	Conder Names	•	6800		
	LOADER		7500		
	SYSTEMS AREA	1	8000		
		25			

Basic machine; library entirely stored UMOD SPISON

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FIGURE 2.



		<u>Addres</u> 8	<u>s</u>	Remarks
•	INTERPRETER			
	- B 8	8		
			×	
	OBJECT CODE	4200		
	CONSTANTS	5300		Labelled QACODL
	DETACHED LIBRARY MACHINE CODE	5500 5700		If any is present
	SCALARS	6100	٠	Labelled QAVNDA
	STACK	6300		u j
	A A			
	LOADER NAMES			
	LOADER	7500		· · · · · ·
	SYSTEMS AREA	8000		

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Multi module machine; suggested scheme

INTERPRETER
LIBRARY
SPARE FOR SPECIAL MACHINE CODE 1 1 LOADER NAMES LOADER SYSTEMS AREA
OBJECT CODE
CONSTANTS SCALARS STACK
AD LIB
1

Address 8

4200

4900

7500 8000

8192

12 -

OBJECT CODE

5.1 As held in the store

The object code consists of 18 bit words which are interpreted at run time. Like machine code each word has a 5 bit (mnemonic) function part and a 13 bit address which is often a data reference:-

TIR N "Take Integer Result from location N in the Notional Data Area and store it in the stack; advance the stack pointer by three".

When the address is not a data reference then it is either a small integer:-

INDR 3n "Store in the stack an element from an n dimensional array".

or it is the address of part of the object program being executed:-

IFJ N "If False Jump. The top-most stack location is regarded as a boolean result; reduce the stack pointer by three and, if false, jump to address N from the beginning, of the object code".

or it is some miscellaneous quantity, sometimes a packed pair of items.

Of the 32 possible functions there are about 30 which are described below; one of these, called PRIM, is used to reach one of about 60 further subroutines and the entire collection of 90-odd subroutines forms the bulk of the interpreter.

Example, if a and b are integers, the Algol text:-

b: = a+5;

is translated into the following object code:-

TIA	"b"	Take Integer	Address of	b
TIR	"a"	Take Integer	Result	a
TIC	"5"	Take Integer	Constant	5
PRIM	I+I	Add Integers		
PRIM	ST	Store result	in b.	

The references to b, a, 5, I+I and ST are all small integers in the actual code.

5.2

As punched on paper tape

There are two formats on the paper tape corresponding to the two input modes for the 920 and 903. 920 mode (7 bits in) 903 mode (8 bits in) A B C D E . F G H I J K L M . N O P Q R S T U . V W X direction

of motion

A B C \leftarrow Loader code \rightarrow B C D

D L T \leftarrow Parity bits \rightarrow A I Q and are all zero.

EFGHIJKNNOPQRSUVWX ← Word → EFGHJKLMNOPRSTUVWX itself

It will be noted that in 903 mode the parity bits are all zero; in 920 mode the parity bits are correct on tape, but ignored by the hardware. However there is a checksum accumulated for every row of tape for protection against faulty punching.

The following loader facilities are used:

code 1 Load as it stands

code 2 Load after adding base address

Update an implicit jump such as is needed around a procedure body or along a conditional expression. Code 3 accompanies the word containing the address of the word to be updated. For such an update the following word is zero and is accompanied by loader code zero and this is followed by three blanks. Code 3 followed by a non zero word is

used to update an array or procedure checking word (see 8 below).

- With subcode 1 for the left hand global labels "qacodl and "qavnda"; with subcode 2 for punching the call of library procedure names. These names are never accompanied by an increment.
- code 5 Skip n locations. This is used to reserve data space at the end of the program.
- code 6 This is a checksum.
- code 7 Stop loading and print "FIRST NEXT" message.

Generally speaking the chaining facility of the loader is only used for multiple calls on a library procedure where this latter is attached to the end of the pord tape.

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code 4

code 3

- 1871 (m. 11

ELLIOTT 903 ALGOL

Addition to Pord Manual December 1966

Section 6

The code number 5 has been allotted to a new function, INDFS, Index Formal Switch. It is punched with loader code 1 and has BN, n as its address part.

Action

"Find FP using BN; address := contents of (3n + FP); subtract 3 from SP; get integer from stack;

If integer is zero or negative or greater than contents of address then FAIL;

store 2 * integer - 1 + address at SP; add 3 to SP; "

Purpose

INDFS is needed at the call of P in the program below.

TEST;

begin

switch S := L1, L2, L3; procedure P (A); value A; label A; goto A; procedure Q (B); switch B; P (B [2]); comment start of program; Q (S); L1 : L3 : print "L` BAD`; stop; L2 : print "L` GOOD`;

end;

D. Hunter

FUNCTION CODES

6.1 Glossary

PP Pord Pointer value 13 bits SP Stack Pointer " 16 11 EP Entry Pointer 11 16 11 11 FP Formal 11 16 11 BN Block Number 11 9 stored in bits 6-14 inc. N A relative address 13 bits n A parameter number 4 11 stored in bits 15-18 inc. m The number of parameters 11 The number of dimensions d 6 bits stored in bits 6-12 inc. 6 bits stored The number of arrays a in bits 13-18 inc. PBA Primitive Base Address 13 bits (= address of beginning of a table of subroutine entry addresses QACODL Address of start of 13 bits Object Data Load QAVNDA Address of start of 13 bits Notional Data Area P input output parameter a small integer BA Base Address of programs 6.2 Names of codes Alphabetically listed names for 5 bit pord functions. Mnemonic Explanation Notes name CF Call Function CFF Call Formal Function . GT Go To Refers to labels area GTF Go To Formal GTFS Go To Formal Switch GTS Go To Switch IFJ If False Jump Conditionals IFUN Integer Function INDA Index Address Subscript references Index Result INDR Index Switch INDS INOUT Input/Output MAMPS Make Array Maps At array declaration

۰.

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Mnemonic <u>name</u>	Explanation	Notes
PE	Procedure Entry	
PEM	Procedure Entry machine	
DDTM	code	
PRIM	Primitive Entry	
RFUN	Real Function	×
TA	Take Address	In program
TF	Take Formal	Parameter references
TIA	Take Integer Address	
TIC	Take Integer Constant	
TICA	Take Integer Constant	
	Address	
TIR	Take Integer Result	
TLA	Take Label Address	
TRA	Take Real Address	
TRC	Take Real Constant	
TRCA	Take Real Constant Addre	
TRCN	The second	
TRR	Take Real Result	
UJ	Unconditional Jump	
(TRAP	Take Real Address in Program)	Hand pording only

6.3	Address	parts
		and the second sec

Mnemonic	Loader 	Function 	Punched Address	Referenced Address
CF	2 or 4	21	N or NAME	N+BA or library NAME entry
CFF	1	22	BN,n	3n+FP
GT GTF GTFS GTS	1 1 1	10 11 14 9	N BN,n BN,n N	N+QACODL 3n+FP " N+QACODL
IFJ IFUN INDA	1 or 2 1 1	7 28 12	8191 or N BN,n 3n	N+BA 3n+FP SP-3n etc see below
INDR INDS INOUT	1 1 1	13 27 15	" P	" N+QACODL see below
MAMPS	1	6	d,a	see below
PE PEM PRIM	1 1 1	23 30 31	BN,n n N	N+PBA
RFUN	1	29	BN,n	3n+FP

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Mnemonic	Loader code	Function code	Punched Address	Referenced Address
TA	2	0	N	N+BA
TF	1	24	BN,n	3n+FP
TIA	1	1	N	N+QAVNDA
TIC	1	18	N	N+QACODL
TICA	1	17	N	N+QACODL see also TLA
TIR	1	2	N	N+QAVNDA
TLA	1	17	N	N+QACODL see also
				TICA
TRA	1	3	N	N+QAVNDA
TRC	1	20	N	N+QACODL
TRCA	1	19	N	n
TRCN	1	26	BN,n	3n+FP
TRR	ī	4	N	N+QAVNDA
UJ	1 or 2	8	8191 or N	N+BA
(TRAP	2	16	N	N+BA cf TA)

6.4 Actions specified

CF The intention of this is to make a partial stack entry before jumping to a procedure body.

"address:= N+BA;

L1: store EP at SP; store SP in Entry Pointer register; add 1 to SP; store PP at SP; add 1 to SP; PP:= address;"

- CFF This is for a call of a formal function. "Find FP using BN; address:= contents of 3n+FP; go to L1 in CF above;"
 - GT An ordinary GO TO is executed by reference to two words in QACODL, of which the first contains the label address and the second the Block Number. The Block Number is needed in case a jump to an outer block is involved.

"address:= N+QACODL; L2: if BN equals contents of (address + 1) then go to contents of address;

L3: if contents of (EP+3) equals contents of (address + 1) then to to L4; EP:= contents of EP; go to L3;

L4: if contents of (EP+2) <0 then go to L5; SP:= contents of (EP+2); FP:= SP-3; go to L6;

- L5: SP:= EP;
- L6: EP:= contents of EP; BN:= contents of (EP+3); go to contents of address;"

- GTF "Find FP using BN; address: = contents of (3n+FP); go to L2 in GT above;"
- GTFS "Find FP using BN; address: = contents of (3n+FP); go to L7 in GTS below;"
- GTS "address: = N+QACODL; L7: subtract 3 from SP; get integer from stack; if integer is zero or greater than contents of address then FAIL; address: = 2 x integer -1 + address; go to L2 in GT above;"
- IFJ "subtract 3 from SP; get boolean (integer) from stack; if zero then go to address N+BA;"
- IFUN This stacks the address of a function name if n = o or of a parameter if $n \neq o$.

"Find FP using BN; store(3n+FP) at SP; add 1 to SP; store the constant +1 at SP; add 2 to SP;"

The constant +1 indicates that the address is that of an integer or boolean variable; this is checked at a procedure entry against the parameter checking word (see 8 below).

- INDA The purpose of this is to place the absolute address of an array element in the stack having been given the address of the map entry and n index values. If the array has real elements then the most significant digit of the result is a one, picked up from the array map. Next to this is placed +1 for an integer or +2 for a real array element. See 8 below.
- INDR As for INDA except that the value of the array element is brought to the stack and unpacked into 3 locations if real.
- INDS "address: = N+QACODL; subtract 3 from SP; get integer from stack;

if integer is zero or greater than contents of address then FAIL;

store 2 x integer -1 +address at SP; add 3 to SP;"

INOUT See section 9; the address part of the pord, p, determines the action in an identical way to that of the address part of a PRIM pord.

MAMPS This operation is executed immediately after entry to a block in which local arrays are declared. The number of dimensions, d, and arrays, a, are packed with 6 bits each at the right hand end of the word containing MAMPS.

bea	<u>gin real</u>	array	A,B,C,D [P:Q, R:S, T:U];
is	translate	d to	
	TIR	Р	
	TIR	Q	
	TIR	R	
	TIR	S	
	TIR	т	
	TIR	U	
	MAMPS	3,4	×
A:	/0	0	/ indicates real //
	3	7	3 dimensions; 7 locations /
			onwards to map address $/_{PA}$.
B:	/0	0	Print .
	3	5	
C:	/0	0	
	3	3	
D:	/0	0	
•	3	1	J
	+0		Map address filled in here
			by action of MAMPS (equal to SP).

The indications "A:" etc do not mean that a global label is punched, merely that reference to array A is by specifying the address of the "array pair" marked "A:".

Using the top six entries in the stack an array map is calculated and placed in the stack, advancing SP appropriately. The array map for a d, dimensional array contains 2d + 1 words in all. These are arrayed as follows (for d =3):-

array map :	TOTAL SIZE	95 .
	OFFSET	> o.K.
	ll	. fren
	cl	OFFSE T>
	12	mfc sinc !
	c2	. / .
	13	

Note that there is one more lowbound value, l, than map coefficient, c. TOTAL SIZE is the number of locations occupied by the array itself. OFFSET is the number of locations between the leading element of the array and the element all of whose index values are zero; it is positive if the element with zero indices lies at a higher address than the leading element.

> * now 2d+2 words because the number of dunensions is stored in front of TOTAL SIZE Action different. A "pointer pair" is created in QAVNDA - fint pointer points to array, second to map.

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The coefficients cl, c2 etc are calculated as follows:-

cl is the range of the first subscript multiplied by two if the array is real.

c2 is the range of the second subscript multiplied by cl.

The lowbound values l_1 , l_2 etc are not necessary for the evaluation of the address of an element of the array, but are necessary for the standard procedure lowbound.

The address of the leading element of each array is filled in at A:, B: etc., the first being equal to SP, the next to SP + TOTAL SIZE etc. OFFSET is then calculated

 $OFFSET = -2 \times P - R \times c1 - T \times c2$

Finally there is a jump to execute the next pord beyond the map address.

The address of element B [i,j,k] is the contents of location B: with its "/" to indicate real plus OFFSET plus 2i + j x cl + k x c2

PE

This operation is at the head of a block bearing the number BN' and expecting m parameters. The action is to complete the stack entry set up by CF or by CFF, and do parameter checking (see 8 below).

BN occupies 9 bits, m 4 bits.

"store EP-3m at SP; add 1 to SP; store (old) BN at SP; add 1 to SP; store BN' in Block Number register; store EP-3m-3 in Formal Pointer register;"

PEM This is placed at the head of a piece of machine code to make it look like pords when called from within the User's Algol program.

Example.

[ARCTAN]		
ARCTAN	PEM 1	
	+0	for return address
	• • •	
	• • •	
	O ARCTAN+1	normal exit in machine
	/8 1	code

The stack entry is completed as for a PE operation but using the current value of BN in place of BN'. There is then a call of the machine code from within the interpreter. On returning to the interpreter in the normal way for a machine code subroutine the action for RETURN (see7.2 below) are executed.

- PRIM Enter a subroutine at address N+PBA.
- RFUN This stacks the address of a real type procedure or a real parameter so that an assignment can be made to it.

"find FP using BN; store FP+3n + /0 0 at SP; add 1 to SP; store /0 2 at SP; add 2 to SP;

The sign bit on the address word indicates a real address, and the sign bit on the +2 indicates an unpacked quantity. The +2 indicates a real quantity for parameter checking (see 8 below).

- TA "store N+BA at SP; add 3 to SP;"
- TF "find FP using BN; store contents of (3n+FP) at SP; store contents of (3n+FP+1) at SP+1; store contents of (3n+FP+2) at SP+2; add 3 to SP;
- TIA "store N+QAVNDA at SP; add 1 to SP; store +1 at SP; add 2 to SP;"
- TIC "store contents of (N+QACODL) at SP: add 3 to SP:"
- TICA) "store N+QACODL + the constant 8 0 at SP; TLA) add 1 to SP; store +1 at SP; add 2 to SP;"

The +1 written by TLA gets.overwritten by +9 if a label is an actual parameter.

- TIR "store contents of (N+QAVNDA) at SP; add 3 to SP;"
- TRA "store N+QAVNDA + the constant /0 0 at SP; add 1 to SP; store +2 at SP; add 2 to SP;
- TRC "store contents of (N+QACODL) at SP; unpack the contents of (N+1+QACODL) and store at SP+1, SP+2; add 3 to SP;"
- TRCA "store /8 0 plus N+QACODL at SP; store +2 at SP+1; add 3 to SP;

- - begin if contents of 3n+FP+1 has sign bit =1 then transfer contents of (address +1) to SP+1 and (address +2) to SP+2 else unpack contents of (address +1) and transfer to SP+1, SP+2 end; add 3 to SP;
- TRR "store contents of (N+QAVNDA) at SP; unpack the contents of (N+1+QAVNDA) and store at SP+1, SP+2; add 3 to SP;

UJ "PP: = N+BA;"

- TRAP "store /8 0 plus N+BA at SP; store +2 at SP+1; add 3 to SP;"
 - This is only for referring to real constants in hand porded programs. This function is not produced by the translator.

PRIMITIVES

7.1 Listed in numerical order

Each primitive is a subroutine and is numbered from 1 to 70; those which are numbered 1-29 and 63-70 are organisational and the remainder are arithmetic, relational, logical or standard procedure routines such as log and exp.

The numberical value of the primitive is used to enter the routine via a table of addresses, and each pord has the function part 31 or /15.

Primitive number	Mnemonic	Explanation
1	CBL	Call Block
2	CHECKB	Check boolean
3	CHECKI	Check integer
4	CHECKR	Check real
5	· CHECKS	Check string
6	DO	
7	STW	Store While
8	FINISH	End of program
9	FOR	Start of FOR statement
10	FR	For Return
11	FSE	For statement end
12	DIV	Integer divide
13	ITOR1	Integer to Real 1

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7.

Doright time number	Maamanda	Pres long tion
Primitive number	Mnemonic	Explanation
14	ITOR2	Integer to Real 2
15	NEGI	
15	NEGT	Negate integer
100 m		
16	NEGR	Negate real
17	RETURN	At end of block
18	RTOI1	Real to integer 1
	RIGII	Meat to integer i
19		
20	ST	Store
		7.8
21	STA	Store Also
22	STEP	
	SILF	
23		
24	WAIT	Wait
25		
26	TIMOT	
	UNTIL	
27	UP	
28	R†I→ R	Arithmetic
29	WHILE	
	I+I→I	withoutin - 31.
30	Τ+Τ→Τ	no spar out
31	$R+R \rightarrow R$	/
32	I-I→I	
22	$R-R \rightarrow R$	
33 34		
34	I*I→I	
35	R*R → R	Arithmetic
36	I/I →R	
		8.
37	$R/R \rightarrow R$	
38	I↑I→I	
39	I↑I→R	
40	$R^R \rightarrow R$	
10		
4.5	T/T D	
41	I <i→b< td=""><td></td></i→b<>	
42	R <r td="" →b<=""><td></td></r>	
43	I≤I→B	Relational
44	$R \leq R \rightarrow B$	
45	$I=I \rightarrow B$	
46	$R=R \rightarrow B$	
47	I≠I→B	
48	$R \neq R \rightarrow B$	
49	I>I→B	Relational
50	R>R→B	
51	I>I → B	
	-	
52	$R \geqslant R \rightarrow B$	
53	$B \land B \rightarrow B$	
53 54	BVB→B	
55	$B \equiv B \rightarrow B$	Logical
<i></i>		DeBrour
56	$B \supset B \rightarrow B$	
57	¬B→B	
58	ABS	
	ENTIER	
59		Others James 1
.69	EXP	Standard procedures
×. •		

.

1

 $\langle \cdot \rangle$

•

•

2

- 23 -

	Prim	itive number	Mnemonio	•	Explanation	
		61	LN		And the function of the state o	
	1×1	62	SIGN		Standard proce	dures
		63	Drow	~		
		64		1	These place in	toron
		65			∞ in stack at	
		0)			where % runs fr	
		66			to 10 for prim	and the second
		67			63-70. The p	
4		68			is for paramet	-
		69			checking.	
		70			encenting.	
		70		-		
	7.2	Actions spe	cified			
	1280					
	1	CBL			ffect as a func	
					ords on and the	refore
			makes a par	rtial sta	ck entry.	
	0 5	OUDOV	When a sum of	. Um ses 1 d m	a catanialil fa	110000
	2-5	CHECK			e, asterisk" fo	
					er or real whos	
					of the stack o	
					s is at the top unchanged excep	
					duces it by 3.	
			101. OUPORD	witten 1.6	duces it by J.	
	6	DO	This makes	use of t	he common proce	dure
	U		ASSIGN (see		no common proce	act o
				002011/1		8
			"ASSIGN;	store PP	at EP+1:	
			store conte	ents of (EP+2) in Pord P	ointer
			register;"	·····		
	7	STW	"ASSIGN;"			
	8	FINISH	Punch 100	rows of b	lank tape, the	word
			FINISH and			
	9	FOR	A stack en	try is ma	de. In the ob	ject
					owed by three p	
			177 (177) 177			
	,		PRIM FOR			
			a ,	address	of controlled s	tatement
			BN			
		21 ×	b	address	of next stateme	ent

"store EP at SP; store (PP+4) at SP+1;* store a + /0 0 at SP+2; store old BN at SP+3; store b at SP+4; EP: = SP; add 5 to SP; store BN'in Block Number register;"

This ends a controlled statement.

"PP: = contents of (EP+1);"

10

FR

PP+4 points to 5 locations after PRIM FOR

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11	FSE	This undoes the effect of FOR and is the last for list element.
		"BN: = contents of (EP+3); PP: = contents of (EP +4); SP: = EP; EP: = contents of EP;"
12	DIV	See arithmetic primitives.
13 14		Convert contents of SP-3 and SP-6 from integer to unpacked real respectively.
15 16		Replace the contents of SP-3 by minus the contents.
17	RETURN	Used at the end of a block or procedure.
	а ж	<pre>"BN: = contents of (EP+3); SP: = contents of (EP+2); PP: = contents of (EP+1); EP: = contents of EP;</pre>
		FP: = contents of (EP+2) minus 3;"
18	RIOI1	Convert contents of SP-3 from unpacked real to integer with FAIL if overflow.
20	ST	This makes use of the common procedure ASSIGN (see below)
	a	"ASSIGN; subtract 3 from SP;
21	STA	"ASSIGN; copy the contents of SP to SP-3 (SP+1) to SP-2 (SP+2) to SP-1;"
22	STEP	"ASSIGN; store PP at EP+1; clear location EP+7;"
24	WAIT	Halt but prepare to resume after a start at 9.
26	UNTIL	"If the contents of (EP+7) are zero then make them non zero else add the contents of (EP+8) to the variable whose address is at EP+5;
		If the sign of (the increment held at EP+8 times (the controlled variable whose address is at EP+5 minus the final value given in (EP+11)) is greater than zero then store PP at EP+1; this corres- ponds to element exhausted;
		subtract 6 from SP;
		If the above sign is $\leq O$ then store the contents of (EP+2) in the pord pointer register; this causes the statement to be executed;"

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The type of arithmetic is determined* by the address of the controlled variable held at EP+5.

27 UP This makes space in the stack for the result of a type procedure.

"Add 3 to SP;"

29 WHILE

If the topmost value is <u>false</u> then the next pord is taken, but if <u>true</u> then a pord jump occurs to the address given in EP+2.

"subtract 3 from SP; if <u>false</u> store PP in EP+1; if <u>true</u> store contents of (EP+2) in Pord Pointer register;"

procedure ASSIGN; "subtract 3 from SP; address: = contents of (SP-3); type: = contents of (SP-2);

> if address has an "8 O" bit then FAIL because assignment to a constant is being attempted;

> store contents of SP at address;

if address < 0 then <u>begin</u>, if type < 0 then

transfer contents of (SP+1) to address+1 and contents of (SP+2) to address +2

else pack together the contents of (SP+1) and (SP+2) with round off and store at address +1

end;"

The primitives numbered 30-56 have two inputs and one output. The left hand input is at SP-6, the right hand input is at SP-3. The output occupies SP-3 or, if real, the three words starting at SP-3. The nett effect of each primitive is to reduce SP by 3. Failure actions are not indicated.

Arithmetic	30-40	(and <u>div</u> no.12)
Relational	41-52	
Logical	53-57	(note 57 is like NEGI)

* Packing is determined by the contents of (EP+6).

The primitive_numbered 58-62 are the ones which execute the Algol procedures which are <u>always</u> in the store. A call of one of these procedures does not produce a PRIM UP as an ordinary procedure call would. The actions of all these primitives are:-

"replace contents of (SP-3) by the function of the contents".

The primitives numbered 63-70 serve the purpose of filling the stack with type information alongside the address of any call-by name actual parameter. This is for parameter checking.

PARAMETER CHECKING

At each procedure entry having m parameters there are m parameter checking words following the "PE BN,m" pord. During the execution of the PE pord, a check is made between the relevant word which describes the formal parameter, and the corresponding actual parameter given in the stack; the check includes number of dimensions or parameters.

No check is made for a formal parameter called by value.

If an array parameter is called by value then the copying of the array is done within the PE pord.

1	4	13	a	checking
4 <u></u> }			•	

r 2. dim

 ∇ =1 if called by value

dim contains the number of dimensions plus the base address of the pord program; this latter is a nuisance but is an inevitable consequence of the way the checking word is updated by the loader program.

value of x

8.

parameter type

value of dim

word

2.

1 [:]	integer or boolean	0
2	real	0
3	integer or boolean array	n+BA
4	real array	n+BA
5	integer or boolean procedure	n+BA
6	real procedure	n+BA
7	procedure	n+BA
8	switch [*]	0
9	label	0.
10	string	0

It will be noticed that all pords which stack a real or integer address also fill the adjacent location with +2 or +1 as in the table above. Pords corresponding to actual ' parameters of types 3-10 are each followed by the appropriate primitives.

INPUT OUTPUT

9.

These operations all have a function part of 15, INOUT and an address part p.

	value	of p	action	*
*	1		read an integer	
*	1 2 3 4 5		read a real	<u>a</u>
	3		print an intege	r
	ú		print a real	-
	5		aligned)	
)			
	6		punch {	global
	7		digits	Bronda
	8		freepoint)	
	6 7 8 9 10		rieepoint j	
	19		•	
	10			
*	11		prefix)	
	12		sameline /	
	13		scaled }	global
	14		reader	6
*	15			within a print list
•	19		prine a sering	Aronan a prano 1200
	16		aligned)	×
	17		punch (local
	18		digits (
	19		freepoint)	
	20			settings from the
				global settings.
	21			
*	22		prefix)	* = 9
	23		sameline (local
	24		scaled }	
	25		reader	
	~)			

The parameters to all the above lie in the stack - if there is one at SP-3 and if there are two at SP-6 and SP-3. The operations with an asterisk * have addresses in the stack and all the others, with the exception of sameline, have values.

All parameters are cleared from the stack by decreasing SP appropriately.

The global and local presumed settings are affected by the operations as indicated.

A string is stored with its opening and closing string quotes packed three characters to a word left justified and space filled; inner strings in the 503 Algol sense are permitted. The inner string is interpreted on output.

LIBRARY AND MACHINE CODE PROCEDURES

The library is a set of machine code procedures each introduced by a PEM pord but there is no reason why hand porded procedures should not be added. A hand porded procedure must start with a PE pord containing a Block Number less than 50 and be followed by the correct parameter checking words.

Example

10.

10.1 As declared near the front of the Algol text.

code

<u>integer</u> procedure SUM (a, b); value a; <u>integer</u> a, b;

algol;

This is not needed for library procedures such as arctan which behave exactly as if such a declaration had already been made before the User's program is read.

10.2 As called from further on in the text

"x: = SUM (y+y, z);" is translated to

TIA Hoc II PRIM UP make room for result "v" TIR "y" T1R PRIM I+I⇒I "z" TIA call by name CF SUM using loader global name PRIM ST

10.3 As offered for assembly by SIR for "SUM: = a+b" [SUM]

SUM	/14	2
	+0	
	0	38
	/0	6

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/4	0
0	38
/1	3
/5	0
0	SUM+1
/8	1

%

of

10.4 As hand porded [SUM] SUM /7 642 B /1 0 C 1 0 /12 640

641

642

30

20

17

18

/10

/15

/15

/15

Block Number 40 Checking words

11.

EXAMPLE TRANSLATIONS

In what follows the pords have been written symbolically as far as possible and in a notation which is close to SIR in the belief that this helps the explanation.

11.1 Notation

The function parts of pords are given mnemonically e.g. TIA, TRR etc.

A variable name in small case replaces its address in QAVNDA e.g. TIA x replaces TIA 5 if the address of x is 5.

A parameter name replaces BN,n.

A primitive name replaces its number e.g. PRIM ST for PRIM 20.

The address part of a pord referring to a constant is given correctly in its absolute form and the value of the constant mentioned in a comment.

The names of switches, labels, arrays and procedures are freely used as local SIR labels for explanatory purposes only.

Implicit forward jumps use a series of local SIR labels beginning R1, R2 for historical reasons.

Global labels are declared for QACODL, QAVNDA, the name of the program itself, any library calls and any calls of machine coded procedures.

The semicolon convention e.g. ;+2 and 84; is used

11.2 Program without a run time block

TEST1;

begin	integer		a, b,		b,	c;	
	b:	=	6;				
*	a:	=	c:	8	b	+5	
end;							

*+0

[TEST1 TEST1	QACODL INPUT TIC	QAVNDA] 20 2	(+3)
	INOUT UJ £''L £3`· £TES £T1 £`	17 9;	
	TA INOUT	4; 15	(PUNCHES PROG NAME)
	TIA TIC PRIM	b 3 ST	(+6)
	TIA TIA TIR TIC PRIM PRIM PRIM	a c b 4 I+I→I STA ST	(+5)
	PRIM	FINISH	· ·
QACODL	+0 +1 +3 +6 +5		
QAVNDA >+4 %			

11.3 Program with run time block.

TEST2;

<u>begin</u>	switch s:	= loop, ret;		
	integer a,	b;		
	ret .: b:	= 4;		
	loop: a: =	b; <u>go to</u> ret		

end;

As for TEST1 for the first 10 pords then

	PRIM UJ PE	CBL R1 816	(BN = 51)	
RET	TIA TIC	b 3	(+4)	
	PRIM	ST		
LOOP	TIA TIR . PRIM	a b ST		
	GT	6	(RET)	
	PRIM	RETURN		
Rl	PRIM	FINISH		
QACODL S	+0 +1 +3 +2 0 +816 0 +816 +4	LOOP RET		4
QAVNDA >+3 %		4		

The only difference between a program translated from Algol and the example of 11.2 or 11.3 as assembled by SIR in relocatable binary form, is that the Algol tape has a checksum inserted just before QAVNDA; if this is not done then the first library tape which gets copied after QACODL will cause a checksum failure at load time.

The remaining examples are usually excerpts from complete programs.

.

TEST3;

begin array a,b,c [1:10, -5, 6];

<u>real</u> x, y;integer i, j; i: = j: = 4; c [i,j]: = 0; x: = a [2, 5];

end;

As for TEST1 and TEST2 then

		PRIM	CBL	
		UJ PE	R1 816	
		TIC TIC TIC PRIM TIC MAMPS	1 3 4 NEGI 5 131	$\binom{+1}{+10}$ $\binom{+5}{+5}$ $\binom{+6}{d=2, a=3}$
	A B C	/0 2 /0 2 /0 2 +0	0 5 0 3 0 1	
		TIA TIA TIC PRIM PRIM	i j 6 STA ST	(+4)
	ŀ	TA TIR TIR INDA TIC PRIM PRIM	C 1 J 6 O ITOR1 ST	$\binom{3n}{+0}$
*	v	TRA TA TIC TIC INDR PRIM	∞ A 7 4 6 ST	(+2) (+5)

	PRIM	RETURN
R1	PRIM	FINISH
QACODL	+0 +1 +3 +10 +5 +6 +4 +2	×
QAVNDA		

>+7 %

11.5 Conditional statement

if x < y then go to CYCLE;

	TIR	36
	TIR	У
	PRIM	$R \langle R \rightarrow B$
	IFJ	R17
	GT	cycle
R17		

11.6 Conditional expression

This illustrates the IFIP rule that the type of an expression can be taken to be real if this depends on something at run time -

i: = if k < 0 then j else j + 2.0;

	2		
	TIA	i	
	TIR	k	•
	TIC	0	(+0)
	PRIM	$I \langle I \Rightarrow B$	
	IFJ	R10	
	TIR	j	
	UJ	R11	*
R10	TIR	j	• .
	TRC	•	(+2.0)
	PRIM	ITOR2	
	PRIM	.R+R→R	
	UJ	;+2	
R11	PRIM	ITOR1	
	PRIM	RTOI1	
	PRIM	ST	

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11.7 Switch

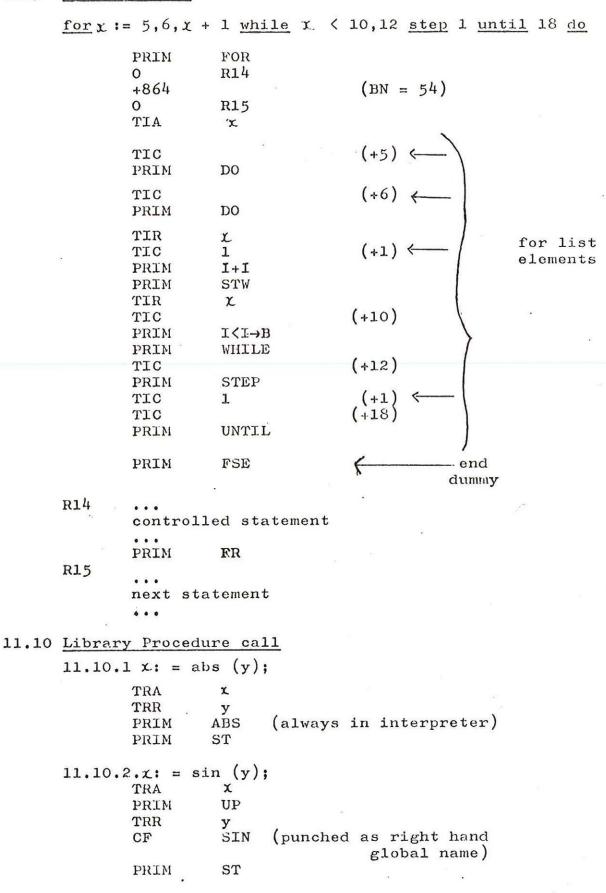
go to	S[k];
TIR	k
GTS	S

11.8 Input output

 anput ou	- protection		
11.8.1	print x*	y, samelir	ne, digits (3), z;
	INOUT	20	(reset settings)
	TIR	x	
	TIR	У	
	PRIM	IxI⇒I	
	INOUT	3	(print integer)
	INOUT	23	(sameline local)
	TIC	2	(+3)
	INOUT	18	(digits local)
	TRR	z	
	INOUT	4	(print real)
11.8.2	read a[i,	j], k;	
	INOUT	20	(reset settings)
	TA	Â	(10000 00001160)
	TIR	i	
	TIR	j	
	INDA	j 6	
	INOUT	2	(read a real)
	TIA	k	
	INOUT	1	(read an integer)
11.8.3	digits (1	+); .	
	TIC		(+4)
	INOUT	7	(digits global)
11.8.4	print ''	L2 TABLE	•
	INOUT	20	
	UJ	R23 [·]	
	£ L		
1	£2`T		
	£ABL		
DOO	£E`	Q1.	Lacourse fill at location
R23	ТА	84;	(assumes £'L at location 84)
	INOUT	15	
			19 - V

11.9 for statement

6



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In this example it is as if SIN had been added to the SIR declarations at the start of the program. The loader does not care whether SIN occurs before or after this point and establishes a chain for a series of calls.

11.11 Procedure body and call

<u>begin integer procedure</u> SUM (a,b); <u>value</u> a, <u>integer</u> a,b;

SUM: = a+b;

Integer x,y;

$$x_{:} = SUM(4,5);$$

end;

	PRIM	CBL	
	UJ	R1	
	PE	816	(BN = 51)
	UJ	R2	
SUM	PE	834	(BN = 52, m=2)
	/1	0	
	1	0	•
	IFUN	832	
	TF	a	
	TRCN	b	
	PRIM	I+I → I	
	PRIM	ST	
	PRIM	RETURN	(END OF PROC BODY)
R2	TIA	x	
	PRIM	UP	
	TIC		(+4) (+5)
	TICA		(+5)
	CF	SUM	
	PRIM	ST	2
	PRIM	RETURN	
R1	PRIM	FINISH	

11.12 Permissible actual/formal correspondence

The general idea is that at the point of call, a value is left in the stack for every formal parameter called by value and an address is left for those which are called by name. Type conversion is explicitly done. The pords at the point of call are shown below:-.

11.12.1 Formal real called by value

Declared real TRR integer TIR and PRIM I TO R1 array element ... INDR and possibly convert. type procedure PRIM UP ... CF and possibly convert. constant TRC or TIC and PRIMITOR1

Formal real on value list TF Formal real called by name TRCN Formal integer etc as real with conversion.

11.12.2 Formal real called by name

Declared real TRA Constant TRCA Formal real on value list RFUN Formal real called by name TF

Inside the body the parameter may appear on the left or the right of a: = sign and it may be called by name or value. These four cases produce pords as follows:-

0	On the	left	On	the	right
By	name	TF		TRCI	V
By	value	RFUN		TF	

For arrays, procedures and strings which all have address among the program pords, a TA pord is used at the original point of call; if the parameter is to be handed on to an inside procedure then at the inner point of call a TF pord is produced.

A label may or may not be on the value list; if so then the actual parameter can be either a label TLA, or the element of a switch TIR, INDS ... It is regretted that it cannot be the element of a formal switch.

Array and procedure parameters must always correspond in type and number of dimensions or parameters. At the point of call the pord which refers to the following actual parameters is followed by the indicated primitive which places an integer in the stack for checking purposes.

boolean	or	integer	array	PRIM	63	+3
		real a	array		64	+4
11	11	integer	procedur	re	65	+5
		real	procedur	re	66	+6
			procedur	re	67	+7
			switch		68	+8
			label		69	· +9
	•		string		70	+10

Inside the procedure body, at the first reference to an array or procedure parameter the checking word which was punched out at the head of the body is altered to indicate the number of dimensions or parameters. This is done by using loader code 3. If there is no reference to the parameter in question, then the address part of the checking word contains +8191 at run time.

DGNH.